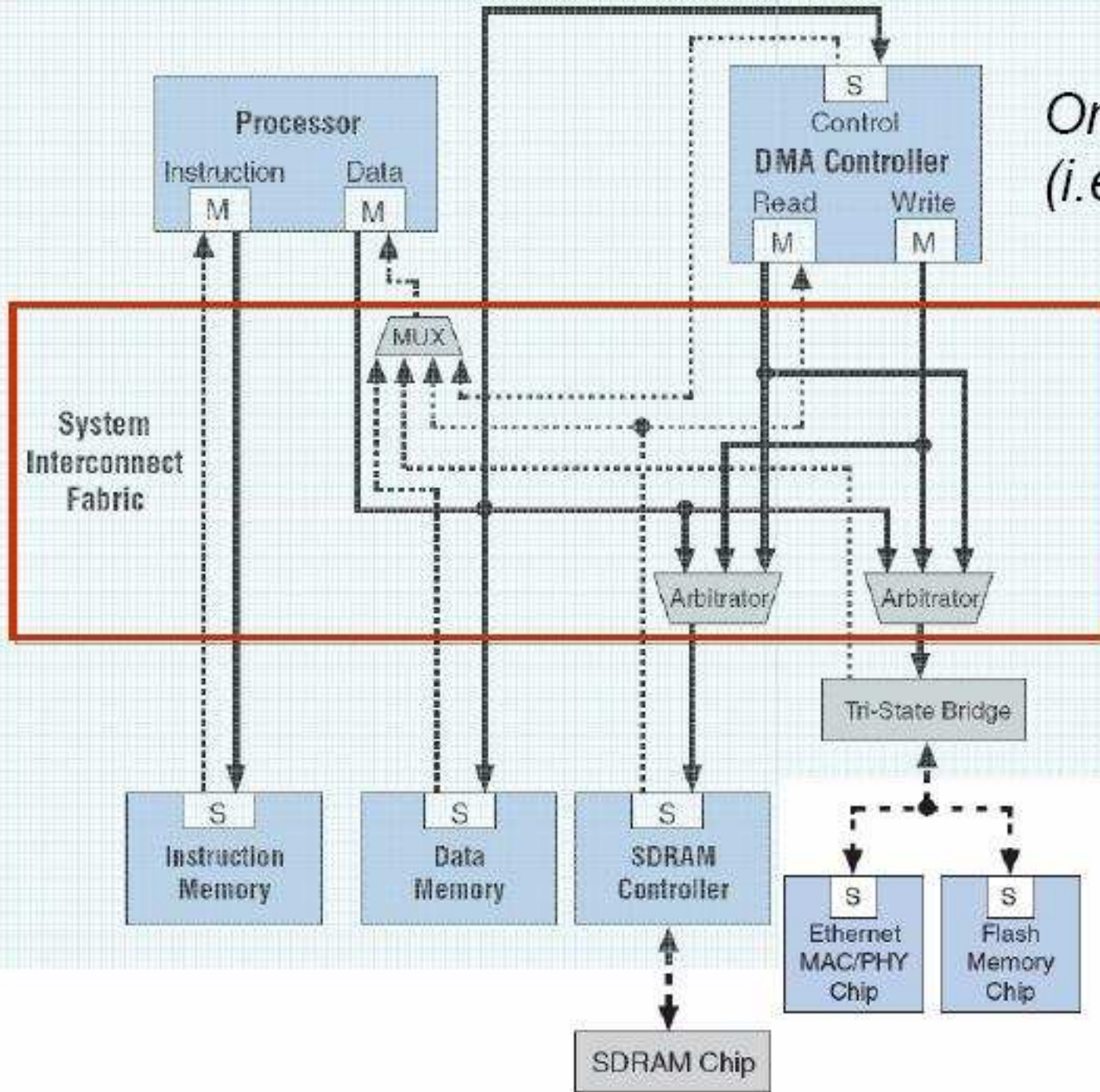


*On-Chip
(i.e. on FPGA)*



- Write Data & Control Signals
- - - → Read Data
- · - · → Interface to Off-Chip Device

- M Avalon Master Port
- S Avalon Slave Port