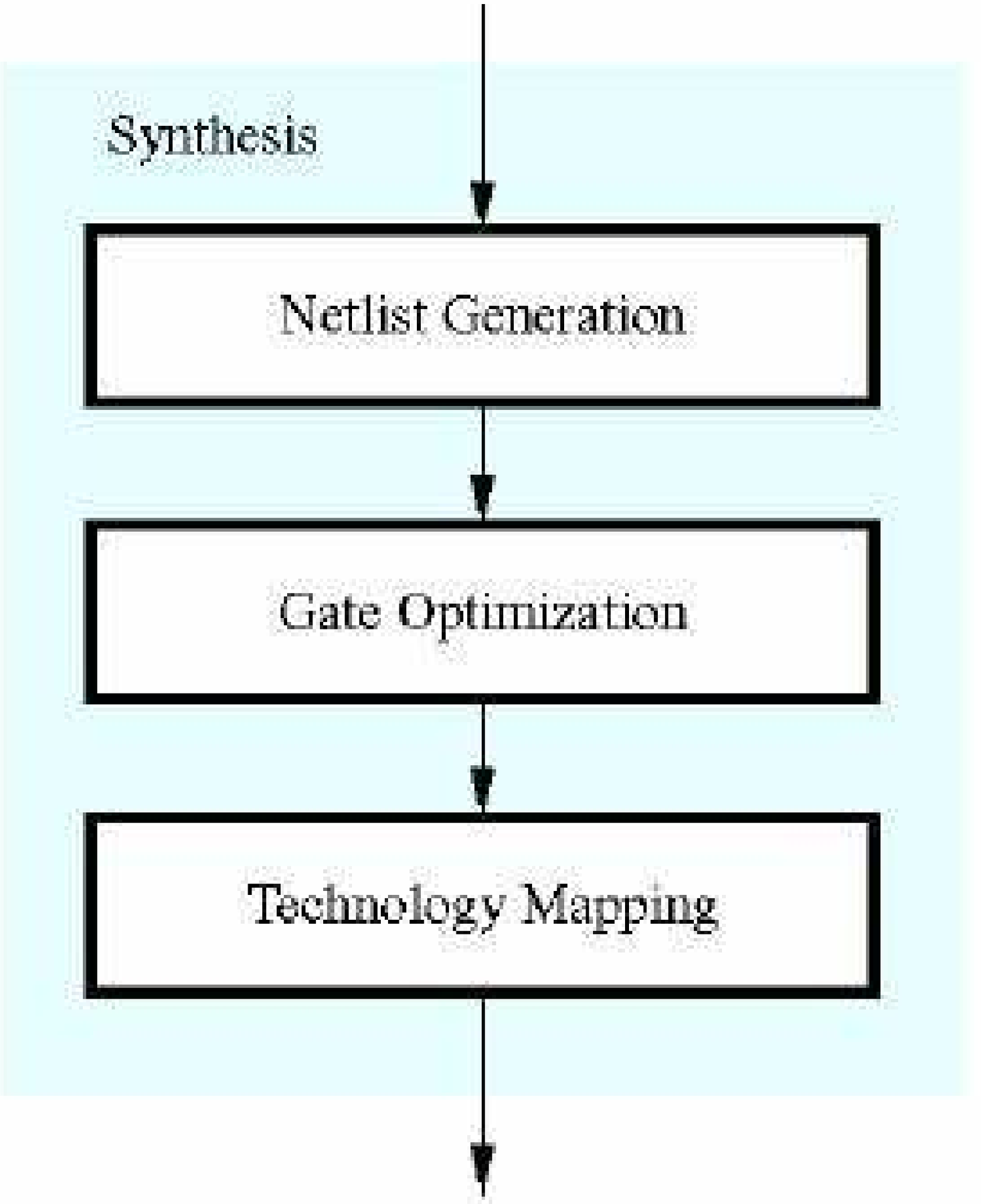
Synthesis
Netlist Generation
Gate Optimization
Technology Mapping