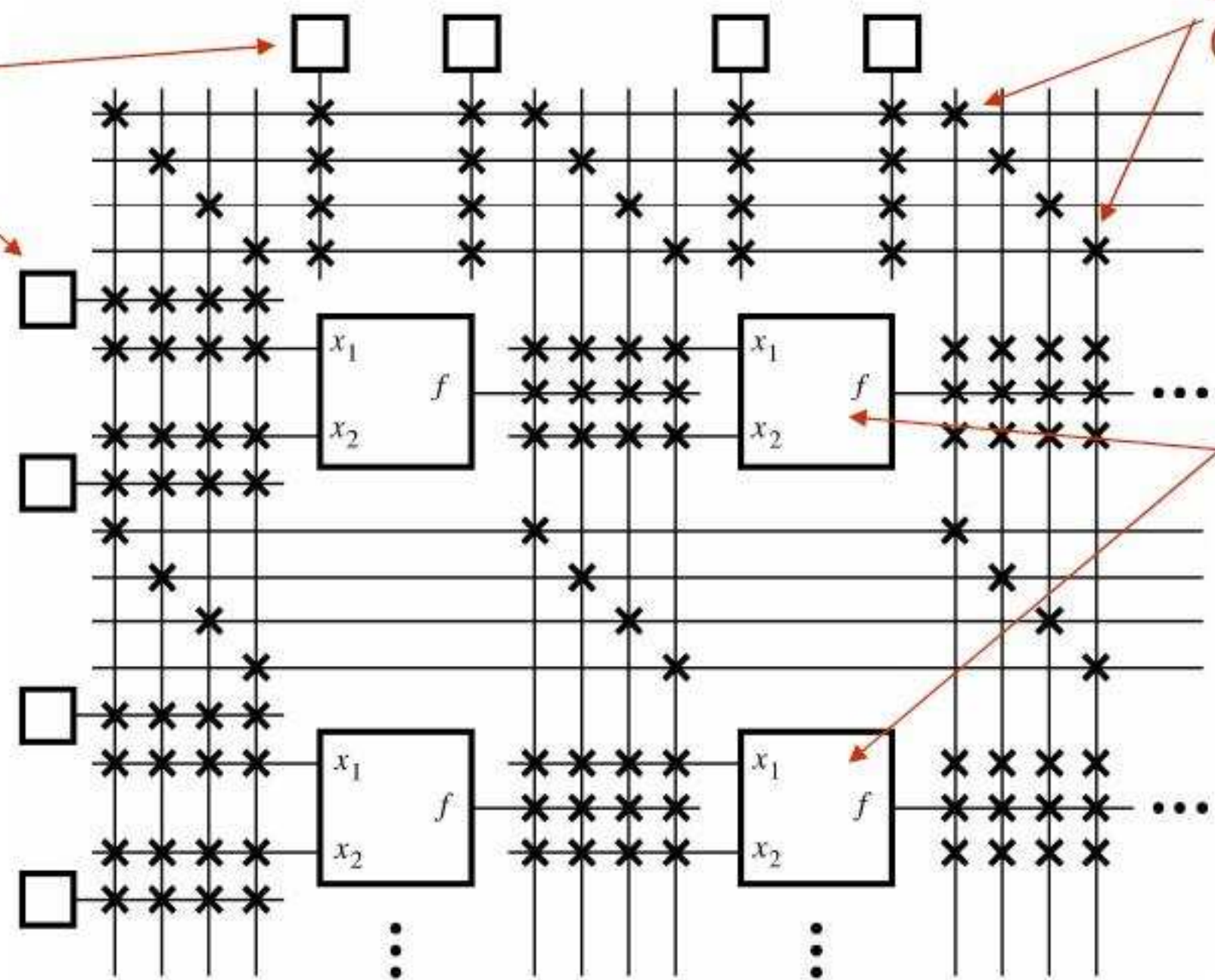


Pins

Programmable Connections



Logic Blocks