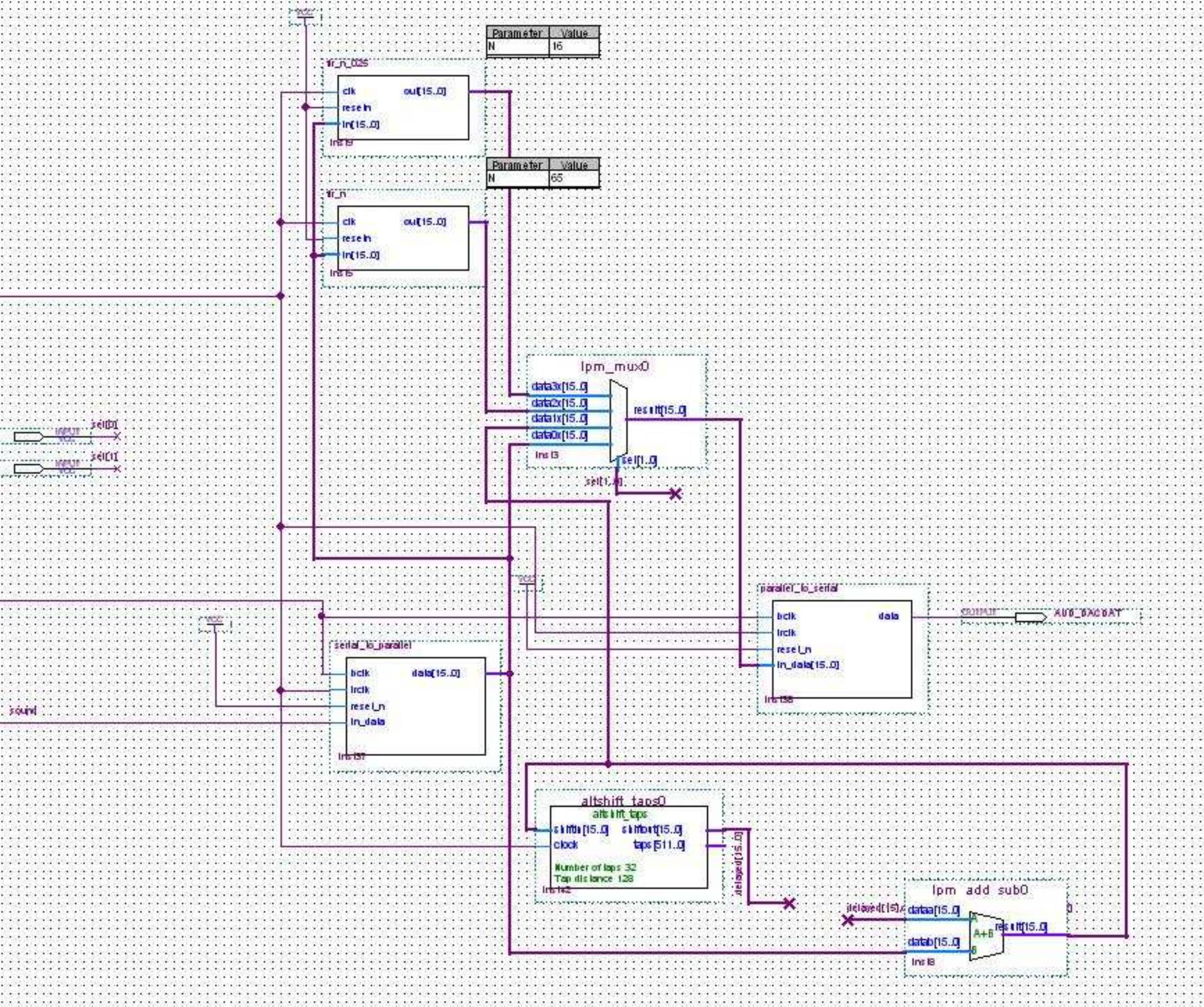
Parameter
Value
N
16
tr_n_025
clk
out[15..0]
resetn
in[15..0]
inst9
Parameter
Value
N
65
tr_n
clk
out[15..0]
resetn
in[15..0]
inst5
lpm_mux0
data3x[15..0]
data2x[15..0]
data1x[15..0]
data0x[15..0]
result[15..0]
inst3
sel[1..0]
sel[1..0]
sel[0]
sel[1]
parallel_to_serial
bclk
data
lrclk
reset_n
in_data[15..0]
inst58
AUD_DACDAT
serial_to_parallel
bclk
data[15..0]
lrclk
reset_n
in_data
inst57
sound
altshift_taps0
altshift_taps
shiftin[15..0]
shiftout[15..0]
clock
taps[511..0]
Number of taps: 32
Tap distance: 128
inst42
delayed[15..0]
lpm_add_sub0
dataa[15..0]
datab[15..0]
A+B
result[15..0]
inst8