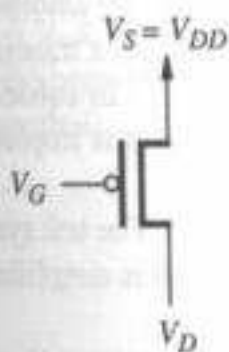


Closed switch
when $V_G = V_{DD}$



Open switch
when $V_G = 0\text{ V}$

(a) NMOS transistor



Open switch
when $V_G = V_{DD}$



Closed switch
when $V_G = 0\text{ V}$

(b) PMOS transistor