

- Altera SOPC Builder
 - Create new component...
 - Nios II Processor
- Bridges and Adapters
- Interface Protocols
 - Ethernet
 - CS8900 Interface (Ethernet)
 - LAN91C111 Interface
 - Serial
 - JTAG UART
 - SPI (3 Wire Serial)
 - UART (RS-232 Serial Port)
- Legacy Components
- Memories and Memory Controllers
 - DMA
 - Flash
 - On-Chip
 - SDRAM
 - SRAM
- Peripherals
 - Debug and Performance
 - Display
 - Character LCD
 - Microcontroller Peripherals
 - Interval Timer
 - PIO (Parallel IO)
 - Multiprocessor Coordination
- PLL

Add...

Target
Device Family: Cyclone II

Clock Settings

Name	Source	MHz
clk	External	60.0

Add Remove

Use	...	Module Name	Description	Clock	Base	End	RC

Remove Edit... Move Up Move Down Address Map... Filter...

Info: Your system is ready to generate

Exit Help Prev Next Generate