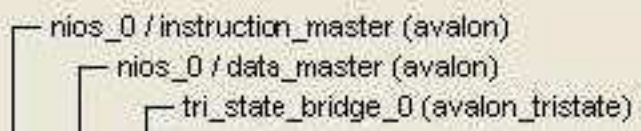


- Altera SOPC Builder
 - Interface to User Logic
 - Avalon Modules**
 - Nios Processor - Altera
 - Bridges**
 - Avalon To AHB Brid
 - Avalon Tri-State Bri
 - Communication
 - Cryptography
 - EP1C20 Nios Developmei
 - EP1S10 Nios Developmei
 - EP1S40 Nios Developmei
 - EP20K200E Nios Developi
 - AMD 29LV800 Flash
 - IDT71V016 SRAM fr
 - Ethernet
 - Math Coprocessors
 - Memory
 - Microcontrollers
 - Other

Target Device Family: System Clock Frequency: MHz



Use		Module Name	Description	Base	End	IRQ
<input checked="" type="checkbox"/>		nios_0	Nios Processor - Altera Corporation			
<input checked="" type="checkbox"/>		tri_state_bridge_0	Avalon Tri-State Bridge			
<input checked="" type="checkbox"/>		nios_dev_board_eram32_0	IDT71V016 SRAM for EP20K200E Nios ...	0x00000000	0x0003FFFF	
<input checked="" type="checkbox"/>		uart_0	UART (RS-232 serial port)	0x00040000	0x0004001F	16
<input checked="" type="checkbox"/>		uart_1	UART (RS-232 serial port)	0x00040020	0x0004003F	17
<input checked="" type="checkbox"/>		timer_0	Interval timer	0x00040040	0x0004005F	18
<input checked="" type="checkbox"/>		SetPos	PIO (Parallel I/O)	0x00040060	0x0004006F	
<input checked="" type="checkbox"/>		SetTgt	PIO (Parallel I/O)	0x00040070	0x0004007F	
<input checked="" type="checkbox"/>		SetCtrl	PIO (Parallel I/O)	0x00040080	0x0004008F	
<input checked="" type="checkbox"/>		ReadPos	PIO (Parallel I/O)	0x00040090	0x0004009F	
<input checked="" type="checkbox"/>		ReadStatus	PIO (Parallel I/O)	0x000400A0	0x000400AF	
<input checked="" type="checkbox"/>		nios_dev_board_flash_0	AMD 29LV800 Flash for EP20K200E Ni...	0x00100000	0x0017FFFF	

All Available Components

Done checking for updates.
 The 2.7 SOPC Builder installation at c:/altera/excalibur/sopc_builder is incompatible and has been ignored. Version 2.8 or later is required.