OpenACC

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Outline

Introduction Execution model Memory model Compiling Example Speedups Profiling **CUDA**

Introduction Execution model Memory model Compiling Example Speedups Profiling $\ensuremath{\mathsf{Why}}\xspace$ accelerators

- If a program execution cannot fit on a single machine and/or many processors are needed: go distributed Message-Passing Interface (MPI)
- If shared memory would do: OpenMP or Pthreads
- Cheaper alternative: accelerators
 - GPUs (NVIDIA, AMD ...)
 - Intel Xeon Phi
- GPUs are not easy to program
 - CUDA supports NVIDIA only
 - OpenCL is portable, harder than CUDA
 - OpenACC
 - Portable, do not need to know much about the hardware
 - Much easier than CUDA and OpenCL

- Set of compiler directives, library routines, and environment variables
- Fortran, C, C++
- Initially developed by PGI, Cray, NVIDIA, CAPS OpenACC 1.0 in 2011 Latest standard 3.3
- Done through pragmas
- We can annotate a serial program with OpenACC directives Non-OpenACC compilers can simply ignore the pragmas

- OpenACC web site http://www.openacc.org/
- Kirk & Hwu. Programming Massively Parallel Processors: A Hands-on Approach
- PGI Accelerator Compilers. OpenACC Getting Started Guide https://www.pgroup.com/doc/openacc_gs.pdf
- PGI compiler and tools https://www.pgroup.com/resources/articles.htm
- OpenACC quick reference http://www.nvidia.com/docs/I0/116711/0penACC-API.pdf
- 11 Tips for Maximizing Performance with OpenACC Directives in Fortran https:

//www.pgroup.com/resources/openacc_tips_fortran.htm

Introduction Execution model Memory model Compiling Example Speedups Profiling OpenACC example: matrix-matrix multiplication

```
#ifdef OPENACC
 1
    #include <openacc.h>
 2
    #endif
 3
 4
    /* A is m \times n, B is n \times p, C = A * B is m \times p * /
 5
    void matmul acc(float * restrict C, float * restrict A, float * restrict B, int m,
 6
                int n, int p) {
         int i. i. k:
 7
    #pragma acc kernels copyin(A[0:m * n], B[0:n * p]) copyout(C[0:m * p])
 8
 9
         ł
             for (i = 0; i < m; i++)
10
                 for (i = 0; i < p; i++)
11
                      float sum = 0:
12
                     for (k = 0; k < n; k++)
13
                         sum += A[i * n + k] * B[k * p + i];
14
                     C[i * p + j] = sum;
15
                 }
16
         }
17
18
    }
```

Introduction Execution model Memory model Compiling Example Speedups Profiling Execution model

An OpenACC program starts as a single thread on the host

- parallel or kernels construct identify parallel or kernels region
- when the program encounters a parallel construct, gangs of workers are created to execute it on the accelerator
- one worker, the gang leader, starts executing the parallel region
- work is distributed when a work-sharing loop is reached

Three levels of parallelism: gang, worker, vector

- a group of gangs execute a kernel
- a group of workers can execute a work-sharing loop from a gang
- a thread can execute vector operations

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Introduction Execution model Memory model Compiling Example Speedups Profiling Memory model

- Main memory and device memory are separate
- Typically
 - $\circ\;$ transfer memory from host to device
 - $\circ~$ execute on device
 - $\circ~$ transfer result to host

Introduction Execution model Memory model Compiling Example Speedups Profiling Compiling

- The Nvidia compilers support OpenACC
- • GCC 12, 11, 10: support OpenACC 2.6
- To compile with nvc,

nvc -fast -acc -Minfo -gpu=cc60 \

-c -o matmul_acc.o matmul_acc.c

- $\circ~$ -fast create generally an optimal set of flags
- -acc generate accelerator code
- -Minfo output compiler info
- -gpu=... GPU type

To find out the GPU type, run pgaccelinfo. Here it gives Default Target: cc60

Compiling with -Minfo outputs

matmul_acc:

- 9, Generating copyin(A[:n*m]) [if not already present] Generating copyout(C[:m*p]) [if not already present] Generating copyin(B[:n*p]) [if not already present]
- 10, Loop carried dependence of C-> prevents parallelization Loop carried backward dependence of C-> prevents vectorization Loop not fused: no successor loop
- 11, Loop is parallelizable Generating NVIDIA GPU code 10, #pragma acc loop seq 11, #pragma acc loop gang, vector(128) /* blockIdx.x threadIdx.x */ 13, #pragma acc loop seq 13, Loop is parallelizable
- 13, Loop is parallelizable Generated vector simd code for the loop containing reductions
- 14, FMA (fused multiply-add) instruction(s) generated

Introduction Execution model Memory model Compiling Example Speedups Profiling OpenMP example

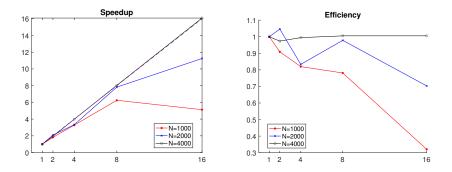
```
#ifdef OPENMP
#include <omp.h>
#endif
void matmul mp(float *restrict C, float * restrict A, float * restrict B, int m,
           int n, int p) {
    int i. i. k:
#pragma omp parallel shared(A, B, C) private(i, j, k)
#pragma omp for schedule(static)
        for (i = 0; i < m; i++)
            for (i = 0; i < p; i++)
                float sum = 0:
                for (k = 0; k < n; k++)
                   sum += A[i * n + k] * B[k * p + i];
               C[i * p + j] = sum;
           }
   }
}
```

Speedup results on Sharcnet's graham 852

32 cores 2 sockets x 16 cores per socket Intel E5-2683 v4 (Broadwell) @ 2.1 GHz 2 × NVIDIA Pascal P100 GPUs (12GB HBM2) Memory: 128.0 GB Introduction Execution model Memory model Compiling Example Speedups Profiling Speedups

	# threads	secs/speedup compared to 1 core		
		Ν		
	р	1000	2000	4000
OpenMP	1	2.0e+00/1.0	1.8e+01/1.0	3.7e+02/1.0
	2	1.1e+00/1.8	8.6e+00/2.1	1.9e+02/1.9
	4	6.1e-01/3.3	5.4e+00/3.3	9.3e+01/4.0
	8	3.2e-01/6.2	2.3e+00/7.8	4.6e+01/8.0
	16	3.9e-01/5.1	1.6e+00/11.2	<mark>2.3e+01</mark> /16.1
GPU		2.1e-01/9.5	3.9e-01/46.2	1.2e+00/308.3

Introduction Execution model Memory model Compiling Example Speedups Profiling Speedup and efficiency of OpenMP code



```
Introduction Execution model Memory model Compiling Example Speedups Profiling
PGI ACC TIME
    To output profiling information, set in Bash
    export PGI_ACC_TIME=1
    Executing ./matmul_acc 1000 gives
    Accelerator Kernel Timing data
    /home/ned/gpu/matmul_acc.c
      matmul acc NVIDIA devicenum=0
        time(us): 1,050
        11: compute region reached 1 time
            14: kernel launched 1 time
                grid: [8] block: [128]
                elapsed time(us): total=50,370 max=50,370 min=50,370
                                 avg=50.370
        11: data region reached 2 times
            11: data copyin transfers: 2
                 device time(us): total=725 max=376 min=349 avg=362
            22: data copyout transfers: 1
                 device time(us): total=325 max=325 min=325 avg=325
```

- block: a a group of threads that are scheduled to execute together
- grid: collection of blocks that are scheduled to execute
- Here 8 blocks \times 128 threads each = 1024 threads

Introduction Execution model Memory model Compiling Example Speedups Profiling $\ensuremath{\mathsf{CUDA}}$

CUDA: Compute Unified Device Architecture

Kernel

- function running on the GPU
- executed by a (1D or 2D) grid of thread blocks
- $\bullet\,$ thread blocks can be 1D, 2D or 3D
 - $\circ~$ execute independently of each other
 - $\circ\;$ threads within a single thread block can synchronize
- grid size and thread block size are defined when a kernel is launched

Programming

- NVIDIA GPUs are programmed as a sequence of kernels
- typically, a kernel completes execution before the next kernel begins
- threads are grouped into blocks, and blocks are grouped into a grid
- a kernel is executed as a grid of blocks of threads
- a thread has a unique local index in its block
- a block has a unique index in the grid

Introduction Execution model Memory model Compiling Example Speedups Profiling

- number of gangs and number of workers in each gang remain constant in a parallel region
- num_gangs clause specifies number of gangs
- num_workers clause specifies number of workers within each gang
- vector_length clause specifies vector length for SIMD operations within each worker of the gang